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2814

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/054,653 Confirmation No.: 9448
 First Named Inventor: Constantin Bulucea Filing Date: 18 January 2002
 Group Art Unit: 2814 Examiner: Farahani, D.
 Atty. Docket No.: NS-5127 US National Docket No.: P05127
 Title: Gate-Enhanced Junction Varactor With Gradual Capacitance Variation
 Assignee: National Semiconductor Corporation

Santa Clara, California

COMMISSIONER FOR PATENTS
 Washington, D.C. 20231

POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST

Sir:

The above-identified assignee, a Delaware corporation, hereby revokes all powers of attorney previously given and appoints the attorney(s) and/or agents(s) identified below to prosecute the above-identified patent application and to transact all business in the United States Patent and Trademark Office ("USPTO") in connection therewith:

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ASSIGNEE CERTIFICATION UNDER 37 CFR 3.73(b)

The undersigned representative of the above-identified assignee certifies that the above-identified assignee is the assignee of the entire right, title, and interest in the above-identified patent application by virtue of a chain of title from the inventor(s) of the above-identified patent application to the above-identified assignee as shown below:

An assignment (potentially executed in counterpart) from each inventor to the above-identified assignee on 3 April 2002, the assignment having been recorded in the USPTO at Reel 12851, Frame 0206.

The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the above-identified assignee.

Signature:  Date: 2/13/03

Name: Christopher J. Byrne

Title: Director of Intellectual Property and Technology Licensing

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